



NATIONAL INSTITUTE OF TECHNOLOGY

(Established by Ministry of Human Resources Development, Govt. Of India)

Yupia, District Papum Pare, Arunachal Pradesh - 791112

Time Table for B.Tech, 1ST YEAR 1ST SEMESTER, CSE/ECE/EEE (JULY-DEC. 2012)

Day	9 am to 10 am		10 am to 11 am		11am to 12 noon		12 noon to 1 pm		1 pm to 2 pm	2 pm to 3 pm		3 pm to 4 pm		4 pm to 5 pm				
MON	PHY 101 PC EEE 101 PDK	SEC-A R 109 SEC-B R 110	BIO 101 AP(Bio)	SEC A R 109	PHY 101 PC	SEC B R 110	EEE 101 PDK	SEC A R 109	CHY 101 NP BIO 101 AP(Bio)	SEC A R 109 SEC B R 110	L U N C H	X	MAS101T UKS MAS 101T KRS	SEC A R 109 SEC B R 110	HSS 101 NR	SEC A R 109		
TUE	PHY 101 PC EEE 101 PDK	SEC-A R 109 SEC-B R 110	MAS 101 UKS MAS 101 KRS	SEC A R 109 SEC B R 110	CHY 101 NP BIO 101 AP(Bio)	SEC A R 109 SEC B R 110		X				HSS 101 NR	SEC B R 110	PC NP PDK+BB	PHY 101 CHY 101 EEE 101	PHY Lab CHY Lab ECE EEE Lab		
WED	PHY 101 PC EEE 101 PDK	SEC-A R 109 SEC B R 110	MAS 101 UKS MAS101 KRS	SEC A R 109 SEC B R 110	EEE 101 PDK PHY 101 PC	SEC A R 109 SEC B R 110	BIO 101 AP(Bio) CHY 101 NP	SEC A R 109 SEC B R 110				HSS 101 NR	SEC A R 109	PC NP PDK+BB	PHY 101 CHY 101 EEE 101	ECE CSE PHY Lab CHY Lab EEE Lab		
THU	EEE 101 PDK PHY 101 PC	SEC A R 109 SECB R 110	BIO 101 AP(Bio) CHY 101 NP	SEC A R 109 SEC B R 110	MAS 101 UKS MAS 101 KRS	SEC A R 109 SEC B R 110	CHY 101 NP BIO 101 AP(Bio)	SEC A R 109 SEC B R 110				HSS 101 NR	SEC B R 110	PC NP PDK+BB	PHY 101 CHY 101 EEE 101	CSE ECE PHY Lab CHY Lab EEE Lab		
FRI	UR		MEC 101		SEC A		Drawing Hall					X	HSS 102		SEC A		R 109	
	AP		MEC 102		SEC B		ME Workshop						AP		HSS 103		SEC B	
SAT	UR		MEC 101		SEC B		Drawing Hall				X	AP		HSS 103		SEC A		R 109
	AP		MEC 102		SEC A		ME Workshop					HSS 102		SEC B		R 110		

SL	SUBJECT CODE	NAME OF SUBJECT	L	T	P	CREDIT
1	MAS 101	Engineering Mathematics – I	3	1	0	4
2	CHY 101	Engineering Chemistry	3	0	2	4
3	PHY 101	Engineering Physics – I	3	0	2	4
4	BIO 101	Life Science	3	0	0	3
5	MEC 101	Engineering Drawing	0	0	4	2
6	MEC 102	Workshop Practice	0	0	4	2
7	EEE 101	Basic Electrical & Electronics Engineering	3	0	2	4
8	HSS 101	Communication Skill	0	0	2	1
9	HSS 102	NSS / NCC	0	0	2	AUDIT
10	HSS 103	Foreign Language (French / Korean) (Audit)	0	0	2	AUDIT



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Time Table for B.Tech, 2nd YEAR 3RD SEMESTER Computer science and Engineering (JULY-DEC. 2012)

Day	9 am to 10 am	10 am to 11 am	11am to 12 noon	12 noon to 1 pm	1 pm to 2 pm	2 pm to 3 pm	3 pm to 4 pm	4 pm to 5 pm
MON	CSE 302 Sec A SKC R 208 CSE 301 Sec B AP2 R 201	HSS 301 Sec A NR R 208 EEE 301 Sec B KB R 201	CSE 303 CSE RSG R 203	MAS 302 CSE UKS R 203	L U N C H	X	CSE 303 CSE RSG Comp. Lab	
TUE	CSE 302 Sec A SKC R 208 CSE 301 Sec B AP2 R 201	HSS 301 Sec A NR R 208 EEE 301 Sec B KB R 201	CSE 303 CSE RSG R 201	Communication KRS CSE Language Lab		X	X	X
WED	CSE 302 Sec A SKC R 208 CSE 301 Sec B AP2 R 201	EEE 301 Sec A KB R 208 HSS 301 Sec B NR R 201	X	X		MAS 301 Sec A KRS R 208 MAS 301 Sec B UKS R 201	EEE 301 KB	CSE C&D Lab
THU	CSE 301 Sec A AP1 R208 CSE 302 Sec B SKC R 201	EEE 301 Sec A KB R 208 HSS 301 Sec B NR R 201	X	X		MAS 302 CSE USK R 203	CSE 302 SKC	CSE Comp. Lab
FRI	MAS 301 Sec A KRS R 208 MAS 301 Sec B UKS R 201	EEE 301 Sec A KB R 208 MAS 301T Sec B UKS R 201	CSE 301 Sec A AP1 R 208 CSE 302 Sec B SKC R 201	CSE 303 CSE RSG R 203		MAS 302 CSE UKS R 203	CSE 301 AP1+AP2	CSE Digital Lab
SAT	MAS 301T Sec A KRS R 208 MAS 301T Sec B UKS R 201	MAS 301T Sec A KRS R208 EEE 301 Sec B KB R 201	CSE 301 Sec A AP1 R 208 CSE 302 Sec B SKC R 201	X		X	X	X

SL	SUBJECT CODE	NAME OF SUBJECT	L	T	P	CREDIT
1	MAS 301	Discrete Mathematics	3	1	0	4
2	EEE 301	Circuit Theory & Network	3	0	2	4
3	CSE 301	Computer Organization & Architecture	3	0	2	4
4	CSE 302	Data Structure & Algorithm	3	0	2	4
5	HSS 301	Behavioural Science	2	0	0	2
6	MAS 302	Optimization Technology	3	0	0	3
7	CSE 303	Object Oriented Programming	3	0	2	4
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Time Table for B.Tech, 2nd YEAR 3RD SEMESTER Electricronics and communication Engineering (JULY-DEC. 2012)

Day	9 am to 10 am	10 am to 11 am	11am to 12 noon	12 noon to 1 pm	1 pm to 2 pm	2 pm to 3 pm	3 pm to 4 pm	4 pm to 5 pm
MON	CSE 302 Sec A SKC R 208 CSE 301 Sec B AP2 R 201	HSS 301 Sec A NR R 208 EEE 301 Sec B KB R 201	ECE 301 EEE+ECE AD R 201	Communication KRS ECE Language Lab	L U N C H	X	ECE 302 ECE AN R 208	X
TUE	CSE 302 Sec A SKC R 208 CSE 301 Sec B AP2 R 201	HSS 301 Sec A NR R 208 EEE 301 Sec B KB R 201	X	X		X	ECE 302 ECE AN ECE Lab	
WED	CSE 302 Sec A SKC R 208 CSE 301 Sec B AP2 R 201	EEE 301 Sec A KB R 208 HSS 301 Sec B NR R 201	ECE 301 EEE+ECE AD R 201	X		MAS 301 Sec A KRS R 208 MAS 301 Sec B UKS R 201	CSE 302 ECE SKC Comp. Lab	
THU	CSE 301 Sec A AP1 R208 CSE 302 SecB SKC R206	EEE 301 Sec A KB R 201 HSS 301 Sec B NR R 208	ECE 301 EEE+ECE AD R 201	X		X	CSE 301 ECE AP1+AP2 Digital Lab	
FRI	MAS 301 Sec A KRS R 208 MAS 301 Sec B UKS R 201	EEE 301 Sec A KB R 208 MAS 301T Sec B UKS R 201	CSE 301 Sec A AP1 R 208 CSE 302 Sec B SKC R 201	X		ECE 302 ECE AN R 208	EEE 301 ECE KB C&D Lab	
SAT	MAS 301T Sec A KRS R 208 MAS 301T Sec B UKS R 201	MAS 301T Sec A KRS R208 EEE 301 Sec B KB R 201	CSE 301 Sec A AP1 R 208 CSE 302 Sec B SKC R 201	ECE 302 ECE AN R 208		X	ECE 301 ECE AD C&D Lab	

SL	SUBJECT CODE	NAME OF SUBJECT	L	T	P	CREDIT
1	MAS 301	Discrete Mathematics	3	1	0	4
2	EEE 301	Circuit Theory & Network	3	0	2	4
3	CSE 301	Computer Organization & Architecture	3	0	2	4
4	CSE 302	Data Structure & Algorithm	3	0	2	4
5	ECE 301	Electronics Circuit & Devices - I	3	0	2	4
6	ECE 302	Electronics Instrument & Measurement	3	0	2	4
7	HSS 301	Behavioral Science	2	0	0	2



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Time Table for B.Tech, 2nd YEAR 3RD SEMESTER Electrical and Electronics Engineering (JULY-DEC. 2012)

Day	9 am to 10 am	10 am to 11 am	11am to 12 noon	12 noon to 1 pm	1 pm to 2 pm	2 pm to 3 pm	3 pm to 4 pm	4 pm to 5 pm	
MON	CSE 302 Sec A SKC R 208 CSE 301 Sec B AP2 R 201	HSS 301 Sec A NR R 208 EEE 301 Sec B KB R 201	ECE 301 EEE+ECE AD R 208	X	L U N C H	X	ECE 301 AD	EEE C&D Lab	
TUE	CSE 302 Sec A SKC R 208 CSE 301 Sec B AP2 R 201	HSS 301 Sec A NR R 208 EEE 301 Sec B KB R 201	EEE 302 EEE PC R 206	X		X	X		
WED	CSE 302 Sec A SKC R 208 CSE 301 Sec B AP2 R 201	EEE 301 Sec A KB R 208 HSS 301 Sec B NR R 201	ECE 301 EEE+ECE AD R 208	Communication KRS EEE Language Lab		MAS 301 Sec A KRS R 208 MAS 301 Sec B UKS R 201	AP1+AP2	CSE 301 EEE Digital Lab	
THU	CSE 301 Sec A AP1 R208 CSE 302 SecB SKC R206	EEE 301 Sec A KB R 201 HSS 301 Sec B NR R 208	ECE 301 EEE+ECE AD R 208	X		X	KB	EEE 301 EEE	
FRI	MAS 301 Sec A KRS R 208 MAS 301 Sec B UKS R 201	EEE 301 Sec A KB R 208 MAS 301T Sec B UKS R 201	CSE 301 Sec A AP1 R 208 CSE 302 Sec B SKC R 201	EEE 302 EEE PC R 206		X	SKC	CSE 302 EEE Comp. Lab	
SAT	MAS 301T Sec A KRS R 208 MAS 301T Sec B UKS R 201	MAS 301T Sec A KRS R208 EEE 301 Sec B KB R 201	CSE 301 Sec A AP1 R 208 CSE 302 Sec B SKC R 201	EEE 302 EEE PC R 206		X		X	

SL	SUBJECT CODE	NAME OF SUBJECT	L	T	P	CREDIT
1	MAS 301	Discrete Mathematics	3	1	0	4
2	ECE 301	Electronic Circuit and Devices	3	0	2	4
3	CSE 301	Computer Org. and Architecture	3	0	2	4
4	CSE 302	Data structure and Algorithm	3	0	2	4
5	EEE 301	Circuit Theory and Networks	3	0	2	4
6	EEE 302	Electromagnetic Field Theory	3	0	0	3
7	HSS 301	Behavioral Science	2	0	0	2



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Time Table for B.Tech, 3rd YEAR 5th SEMESTER Computer science and Engineering (JULY-DEC. 2012)

Day	9 am to 10 am	10 am to 11 am	11am to 12 noon	12 noon to 1 pm	1 pm to 2 pm	2 pm to 3 pm	3 pm to 4 pm	4 pm to 5 pm
MON	CSE 503 CSE+ECE RSG R 203	CSE 501 SKC	CSE Net Lab	HSS 501 Sec A AP(HSS) R 208 ECE 501 SEC B AN R 201	L U N C H	CSE 501 CSE SKC R 203	CSE 504 AP2 Net Lab	
TUE	CSE 503 CSE+ECE RSG R 203	CSE 502 CSE AP1 R 203	CSE 502T CSE AP1 R 203	ECE 501 SEC A AN R 203 HSS 501 Sec B AP(HSS) R 201		X	CSE 504 CSE AP2 R 203	X
WED	CSE 503 CSE+ECE RSG R 203	CSE 503 CSE RSG NET LAB		ECE 501 SEC A AN R 203 HSS 501 Sec B AP(HSS) R 201		X	X	X
THU	X	CSE 504 CSE AP2 R 203	CSE 501 CSE SKC R 203	ECE 501 SEC A AN R 203 HSS 501 Sec B AP(HSS) R 201		X	CSE 505 CSE KRS R 201	X
FRI	HSS 501 Sec A AP(HSS) R 206 ECE 501 SEC B AN R 203	ECE 501 CSE AN Digital lab	X	X		CSE 502 CSE AP1 R 201	CSE 505 CSE KRS R 203	X
SAT	HSS 501 Sec A AP(HSS) R 206 ECE 501 SEC B AN R 203	CSE 504 CSE AP2 R 203	X	X		CSE 502 CSE AP1 R 201	CSE 501 CSE SKC R 203	CSE 505 CSE KRS R 203

ECE – 501	Microprocessors, Microcontrollers & Embedded System	3	0	2	AN
CSE – 501	Operating System	3	0	2	SKC
CSE – 502	Design & Analysis of Algorithm	3	1	0	AP1
CSE – 503	Database Management system	3	0	2	RSG
CSE – 504	System Software & Administration	3	0	2	AP2
HSS – 501	Industrial Management	3	0	0	NR
CSE - 505	Graph Theory & Combinatorics.	3	0	0	KRS



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Time Table for B.Tech, 3rd YEAR 5th SEMESTER Electronics and communication Engineering (JULY-DEC. 2012)

Day	9 am to 10 am	10 am to 11 am	11am to 12 noon	12 noon to 1 pm	1 pm to 2 pm	2 pm to 3 pm	3 pm to 4 pm	4 pm to 5 pm
MON	CSE 503 CSE+ECE RSG R 203	ECE 502 ECE BB	Comn. Lab	HSS 501 Sec A AP(HSS) R 208 ECE 501 SEC B AN R 201	L U N C H	X	ECE 504 ECE BB R 201	X
TUE	CSE 503 CSE+ECE RSG R 203	ECE 503 ECE AD	Comn. lab	ECE 501 SEC A AN R 203 HSS 501 Sec B AP(HSS) R 201		ECE 504 ECE BB R 201	ECE 503 ECE AD R 201	X
WED	CSE 503 CSE+ECE RSG R 203	ECE 501 ECE AN	Digital Lab	ECE 501 SEC A AN R 203 HSS 501 Sec B AP(HSS) R 201		X	X	X
THU	X	CSE 503 ECE RSG	NET LAB	ECE 501 SEC A AN R 203 HSS 501 Sec B AP(HSS) R 201		ECE 502 ECE BB R 201	X	X
FRI	HSS 501 Sec A AP(HSS) R 206 ECE 501 SEC B AN R 203	ECE 504 ECE BB R 206	ECE 504 ECE BB	Comn. Lab		ECE 503 ECE AD R 206	ECE 502 ECE BB R 201	X
SAT	HSS 501 Sec A AP(HSS) R 206 ECE 501 SEC B AN R 203	X	ECE 503 ECE AD R 206	X		X	ECE 502 ECE BB R 201	X

SUBJECT CODE	NAME OF SUBJECT	L	T	P	CRE DIT	
ECE 501	Microprocessor, Microcontroller & Embedded System	3	0	2	4	Digital EC
ECE 502	RF & Microwave Engineering	3	0	2	4	Comn.lab
ECE 503	Digital Communication Engineering	3	0	2	4	Comn.. lab
CSE 503	Database Management System	3	0	2	4	Networking
ECE 504	Digital Signal Processing	3	0	2	4	EC lab
HSS 501	Industrial Management	3	0	0	3	
ECE 505	Telecommunication Engineering	3	0	0	3	



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Time Table for B.Tech, 3rd YEAR 5th SEMESTER (JULY-DEC. 2012) Electrical and Electronics Engineering

Day	9 am to 10 am	10 am to 11 am	11am to 12 noon	12 noon to 1 pm	1 pm to 2 pm	2 pm to 3 pm	3 pm to 4 pm	4 pm to 5 pm	
MON	EEE 505 EEE AD R 206	ECE 501 AN	EEE Digital Lab	HSS 501 Sec A AP(HSS) R 208 ECE 501 SEC B AN R 201	L U N C H	EEE 503 EEE RP R 206	EEE 502 EEE KB Electrical Meas. Lab		
TUE	EEE 504 EEE AD R 206	RP	EEE 501 EEE Electrical M/c Lab	ECE 501 SEC A AN R 203 HSS 501 Sec B AP(HSS) R 201		X	EEE 502 EEE KB R 206	EEE 501 EEE RP R 206	
WED	EEE 505 EEE AD R 206	RP	EEE 503 EEE Electrical Meas. Lab	ECE 501 SEC A AN R 203 HSS 501 Sec B AP(HSS) R 201		X	EEE 504 EEE AD R 206	EEE 501 EEE RP R 206	
THU	EEE 505 EEE AD R 206	RP	EEE 503 EEE R 206	X		ECE 501 SEC A AN R 203 HSS 501 Sec B AP(HSS) R 201	X	EEE 504 EEE AD R 206	EEE 501 EEE RP R 206
FRI	HSS 501 Sec A AP(HSS) R 206 ECE 501 SEC B AN R 203	AD	EEE 504 EEE Ckt & device lab	EEE 502 EEE KB R 208		X	X	X	
SAT	HSS 501 Sec A AP(HSS) R 206 ECE 501 SEC B AN R 203	RP	EEE 503 EEE R 206	X		EEE 502 EEE KB R203	X	X	X

ECE – 501	Microprocessors, Microcontrollers & Embedded Systems	3	0	2	4
EEE 501	Electrical Machine – II	3	0	2	4
EEE 502	Process Control Engineering	3	0	2	4
EEE 503	Switch Gear & Protection	3	0	2	4
EEE 504	Power Electronics	3	0	2	4
HSS 501	Industrial Management	3	0	0	3
EEE 505	Linear Integrated Circuit	3	0	0	3