

Research seminar by members of faculty those who are pursuing their Ph. D was conducted on 1st and 2nd June, 2015 from 10:00 am in the Seminar Hall of the Institute.

Sl No.	Name of the Scholar	Title of the thesis
1	Shri Koj Sambyo, CSE	Study and analysis of reducing delay in cloud computing architecture
2	Shri Subhasish Banerjee, CSE	Study and Development of Various Schemes to Achieve the Perfect Security over Insecure Communication Networks
3	Shri Sahadev Roy	Development of Efficient Minimization Technique for Combinational / Sequential Logic Systems
4	Shri Manash Pratim Dutta, CSE	An approach to develop protocols for Automatic Variable Key (AVK) to get high security and wide flexibility in cryptosystem
5	Yang Saring, ECE	Investigation on new & modified Packet Combining and Aggressive Packet Combining Schemes for Reliable Error Correction with Higher Throughput
6	Yaka Bulu, ECE	Studies on Various schemes of Aggressive Packet Combining and Packet Combining Scheme to Achieve Higher Reliability and Throughput
7	Shri Anish Kumar Saha, CSE	Green Computer Network Architecture: Analysis and Design
8	Shri Abir Jyoti Mondal, ECE	Modeling and Analysis of High Speed Interconnects
9	Shri Alak Majumder, ECE	High Speed Communication between two On-Chip Silicon
10	Shri Achyuth Sarkar, CSE	Studies an analysis of modified Aggressive Packet Combining (APC) scheme with consideration of the physical signals
11	Shri Sandip Mondal	Mixed convective cooling of electronic components with vortex generator